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Form PTO-1449 (modified) ATTY. DKT. NO. 5580-00300 SERIAL NO. 09/684,023 List of Patents and Publications APPLICANT: Rowlands et al. GROUP: 2181 For Applicant's Information Disclosure Statement (Use several sheets if necessary) FILING DATE: October 6, 2000 U.S. PATENT DOCUMENTS FILING DATE IF EXAM. REF. **DOCUMENT NUMBER** DATE **NAME** CLASS SUB APPROPRIATE **INITIALS** DES. **CLASS** MAY 2 5 200 FOREIGN PATENT DOCUMENTS TRANSLATION CLASS **SUB** EXAM. REF. **DOCUMENT NUMBER** DATE **COUNTRY** YES/NO **CLASS INITIALS** DES. OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.) PowerPC™ 601, RISC Microprocessor User's Manual, 6 pgs., © Motorola, Inc. 1993. A1 ملالا Halfhill, "SiByte Reveals 64-Bit Core for NPUs," Microprocessor Report, June 2000, pp. 45-48. A2 Pentium® Pro Family Developer's Manual, Vol. 1: Specifications, Chapter 4, pp. 1-18, 1996. **A3** 

**EXAMINER:** 

Kim T. Huyale DATE CONSIDERED: 3/05/23

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the patent owner.